

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor: Robert M. Zeidman  
Serial Number: 10/044,217  
Filing Date: November 19, 2001  
Title: SYSTEM AND METHOD FOR  
CONNECTING A LOGIC  
CIRCUIT SIMULATION TO A  
NETWORK

§ Atty.Dkt.No.: 6257-16302  
§  
§ Examiner: Luu, Cuong V.  
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§ Group/Art Unit: 2128  
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§ Conf. No.: 9153  
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**\*\*\*\*CERTIFICATE OF E-FILING TRANSMISSION\*\*\*\***

I hereby certify that this correspondence is being transmitted  
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On: December 17, 2010 /Alex A. Courtade/  
Date Alex A. Courtade, #65,635

**RESPONSE TO OFFICE ACTION MAILED SEPTEMBER 17, 2010**

This paper is submitted in response to an Office Action of September 17, 2010, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.